



2183 JFW

Our Docket No: 42390P5563

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
Padwekar, Kiran A. ) Examiner: Li, Aimee, J.  
Application No: 09/608,852 ) Art Unit: 2183  
Filed: June 30, 2000 )  
For: A Branch Target Buffer (BTB) )  
Including a Speculative BTB (SBTB))  
And an Architectural BTB (ABTB) )

**DECLARATION UNDER 37 CFR §1.132**

Commissioner for Patents  
P.O Box 1450  
Alexandria, VA 22313-1450

Sir:

I, Kiran A. Padwekar declare:

1. I am the inventor of the above-mentioned Application, assigned to Intel Corporation of 2200 Mission College Blvd., Santa Clara California 95052 ("Assignee").
2. The invention claimed in the Application is partially disclosed but not claimed in U.S. Patent No. 5,978,909 ("Reference"), which at the time of filing was under an obligation to be assigned to the Assignee.
3. The Application and the Reference are both assigned to the Assignee and thus, the Application is not the invention "by another" under 35 U.S.C. 102(e).

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements are made with the knowledge that willful false statements and the

Declaration Under Rule §1.132  
in Support of Application  
Serial No. 09/608,852

**BEST AVAILABLE COPY**

like so made are punishable by fine or imprisonment, or both under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application of any patent issuing thereon.

Date: 9/27/04

K. A. Padwekar  
Kiran A. Padwekar

Please send all correspondence to:

Blakely, Sokoloff, Taylor & Zafman  
12400 Wilshire Boulevard  
7<sup>th</sup> Floor  
Los Angeles, California 90025-1030  
(303) 740-1980

Declaration Under Rule §1.132  
in Support of Application  
Serial No. 09/608,852

**BEST AVAILABLE COPY**